

# ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

Stylesheet Version v18.0

Title of  
Invention

INTEGRATED CIRCUIT MACRO PLACING SYSTEM AND  
METHOD

Application Number :

10-710701

Confirmation Number:

First Named Applicant:

Robert Allen

Attorney Docket Number:

BUR920040070US1

Art Unit:

Examiner:

Search string:

( 5930151 or 5814846 or 5579237 or 5225991 or 4908772 or 20030204829 or  
20010010090 ).pn

## US Patent Documents

Note: Applicant is not required to submit a paper copy of cited US Patent Documents

init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
1	1	5930151	1999-07-27	Uyama			
2	2	5814846	1998-09-29	Essbaum et al.			
3	3	5579237	1996-11-26	Shibuya			
4	4	5225991	1993-07-06	Dougherty			
5	5	4908772	1990-03-13	Chi			

## US Published Applications

Note: Applicant is not required to submit a paper copy of cited US Published Applications

init	Cite.No.	Pub. No.	Date	Applicant	Kind	Class	Subclass
1	1	20030204829	2003-10-30	Bednar et al.			
2	2	20010010090	2001-07-26	Boyle et al.			

Signature

Examiner Name

Date

*[Signature]*

03/16/06